A 1.2 THz planar tripler using GaAs membrane based chips

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ABSTRACT

Fabrication technology for Submillimeter-wave monolithic circuits has made tremendous progress in recent years and it is now possible to fabricate sub-micron GaAs Schottky devices on a number of substrate types, such as membranes [1], frame-less membranes [2] or substrateless circuits [3]. These new technologies allow designers to implement very high frequency circuits, either Schottky mixers or multipliers, in a radically new manner.

This paper will address the design, fabrication, and preliminary results of a 1.2 THz Schottky tripler fabricated on a GaAs frame-less membrane [2]. This tripler uses a diode pair in an antiparallel configuration similar to designs used at lower frequencies [4]. This configuration has the distinct advantage that only odd harmonics are generated. The availability of the membrane allows one to move the diodes away from the waveguides, to which they are coupled by means of stripline matching circuits and waveguide probes, providing a relatively tolerant circuit. To date, measurement with a fully solid state source has produced a peak output power of 12 μ W at 1130 GHz at 300K, and a record 75 μ W when cooled to 35K. The design of the conceptually simple circuit, together with its fabrication procedure and preliminary results will be presented.

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